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SEP 06 2005

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Timothe LITT	§	Confirmation No.:	1520
Serial No.:	10/034,717	§	Group Art Unit:	2114
Filed:	12/28/2001	§	Examiner:	T. M. Bonura
For:	Method And Apparatus For Efficiently Implementing Trace And/Or Logic Analysis Mechanisms On A Processor Chip	§ § § § § § §	Docket No.:	200302320-1

RESPONSE TO FINAL OFFICE ACTION DATED JUNE 3, 2005

Mail Stop AF
Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

Date: September 6, 2005

AMENDMENT

Sir:

In response to the final Office action of June 3, 2005, please amend the above-identified application as follows:

Amendments to the Claims are reflected in the Listing of Claims that begins on page 2 of this paper.

Remarks/Arguments begin on page 8 of this paper.

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09/12/05